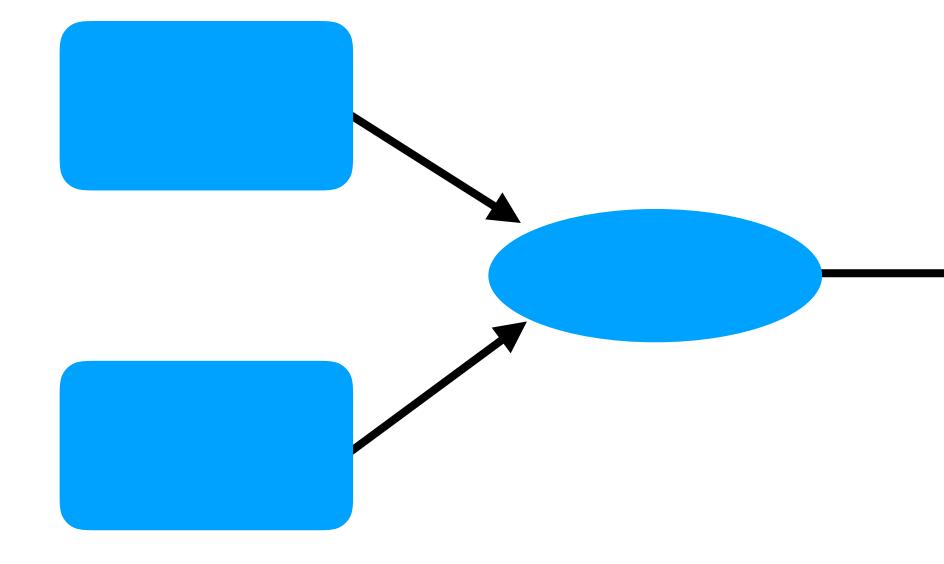
Program Analysis in Relay Gus Smith December 5th, 2019

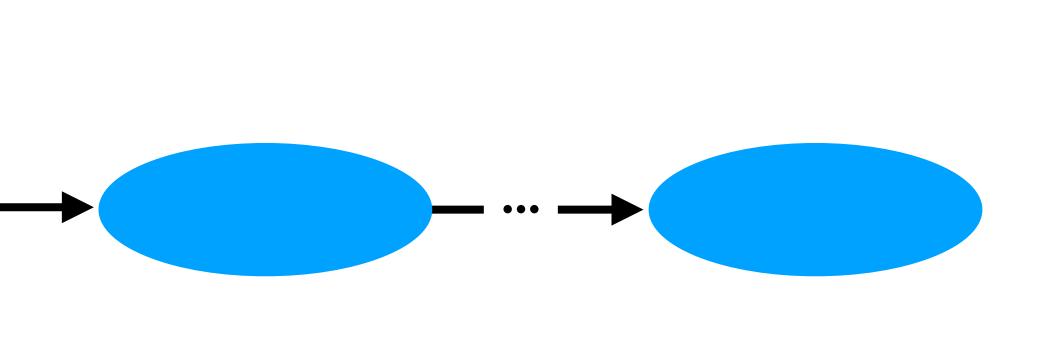


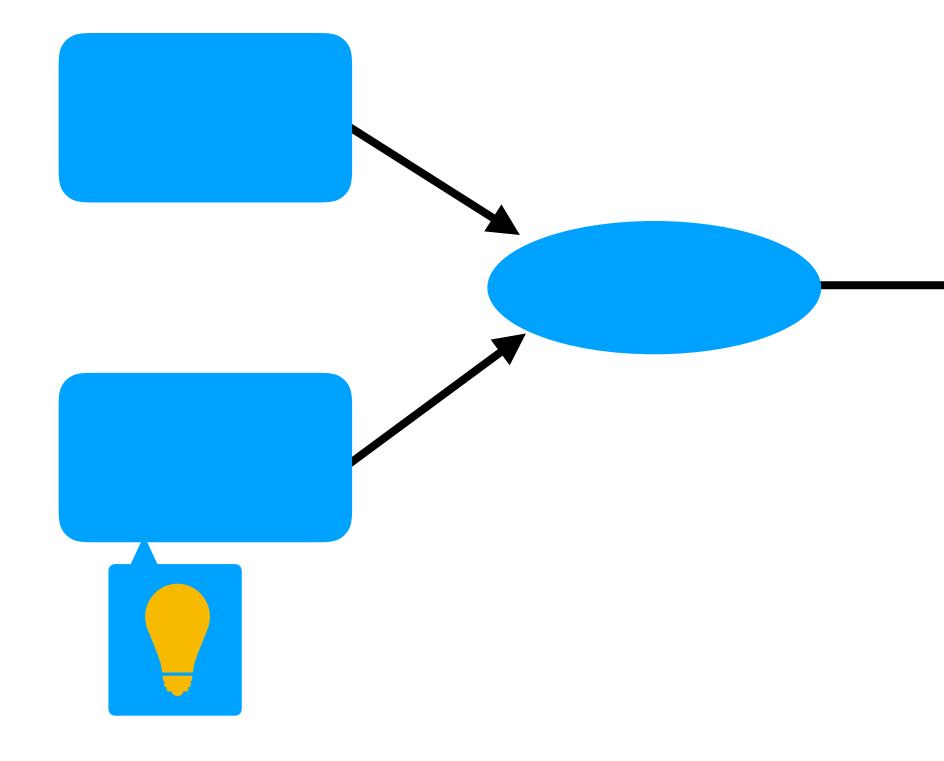


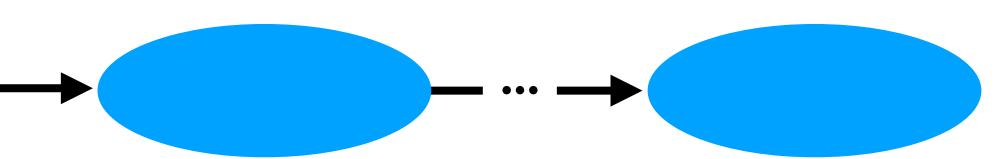


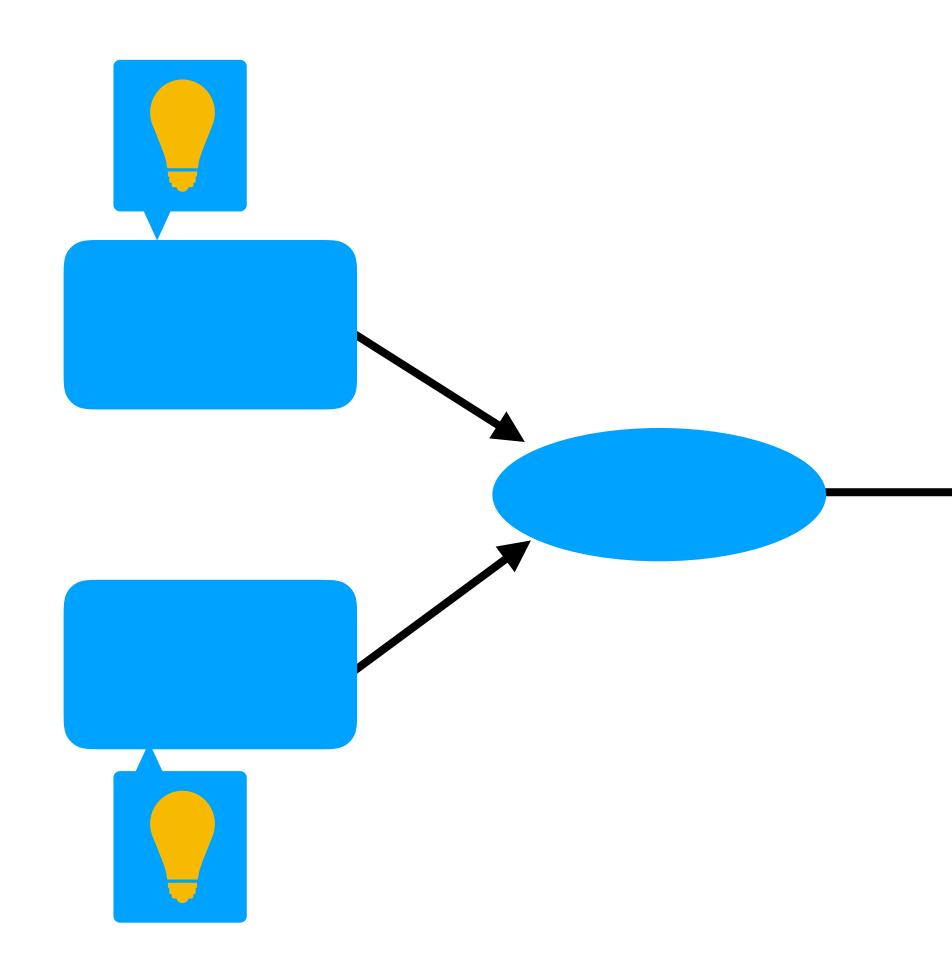


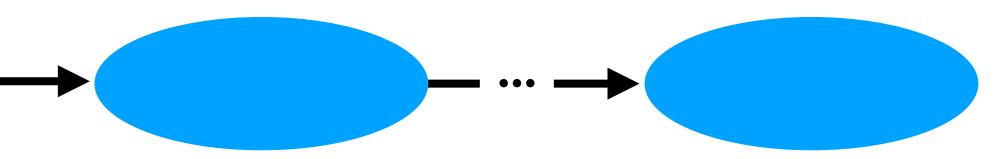


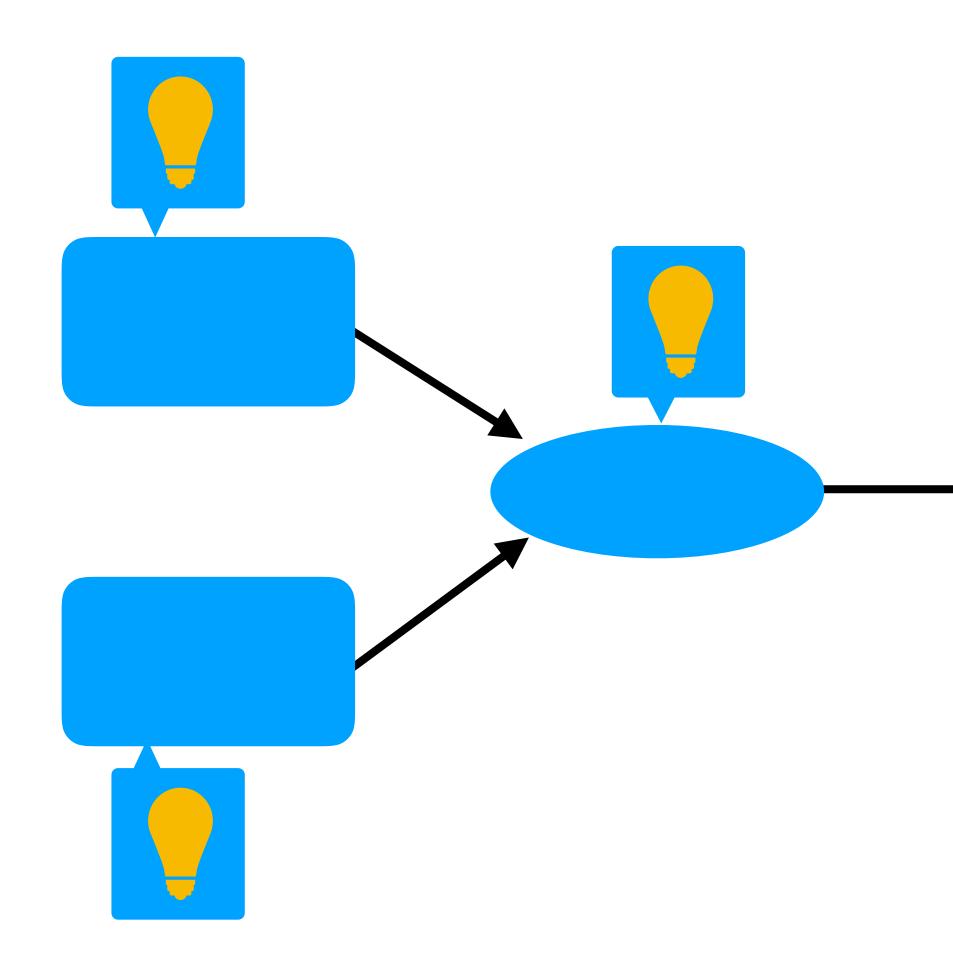


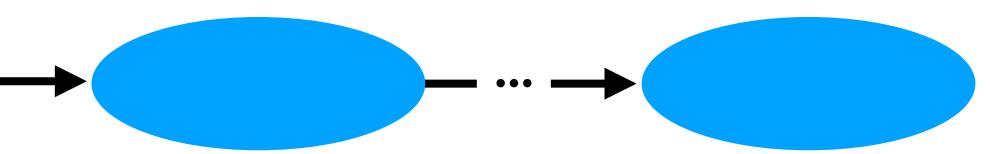


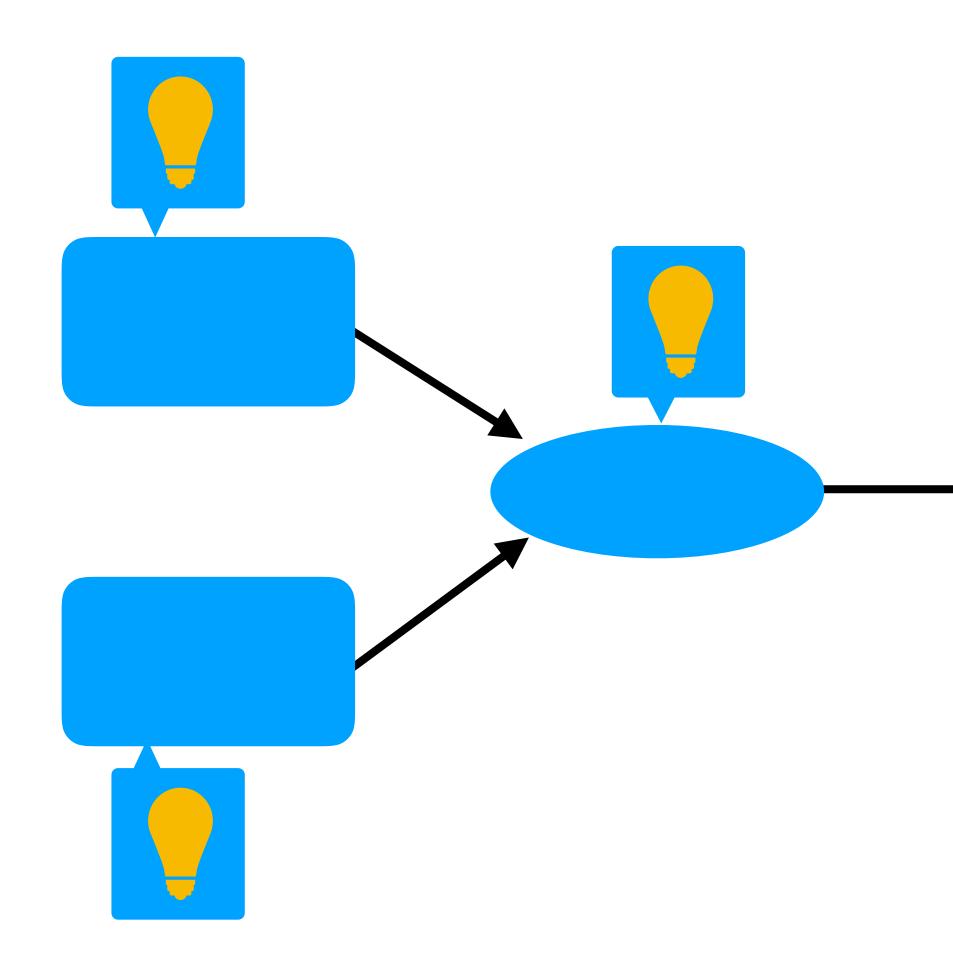


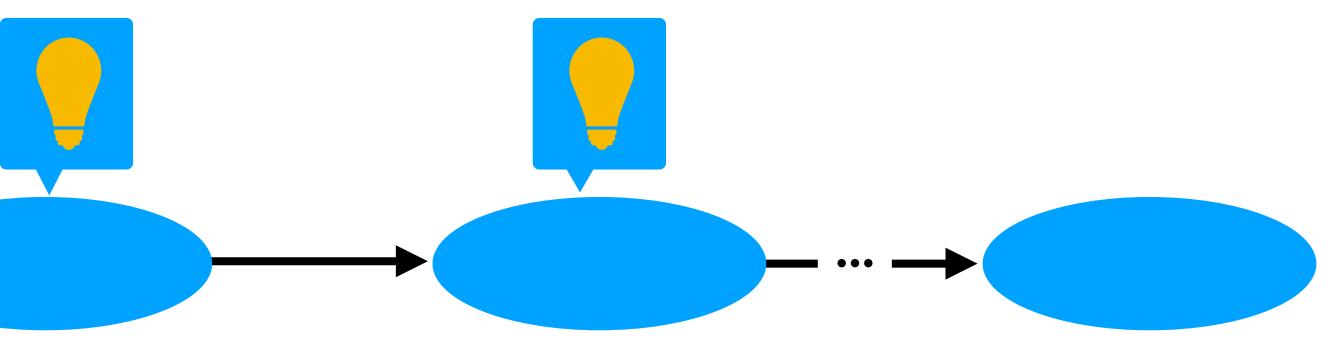


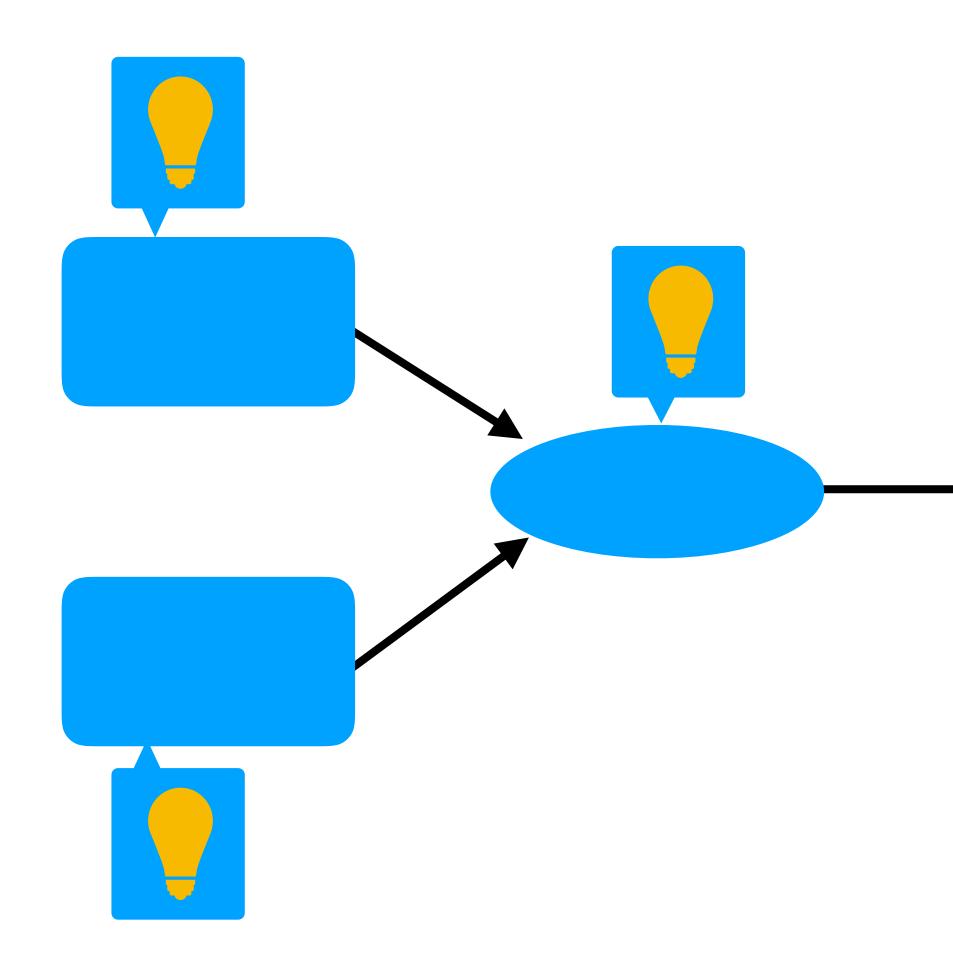


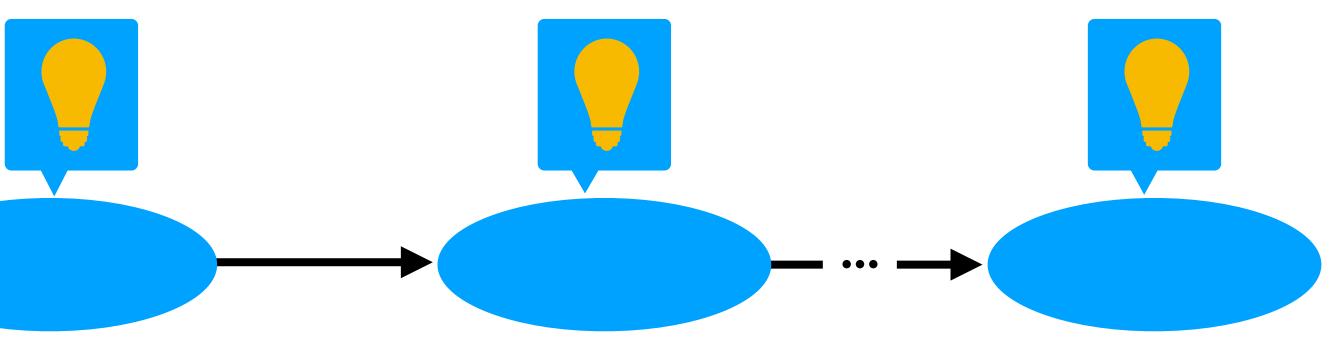


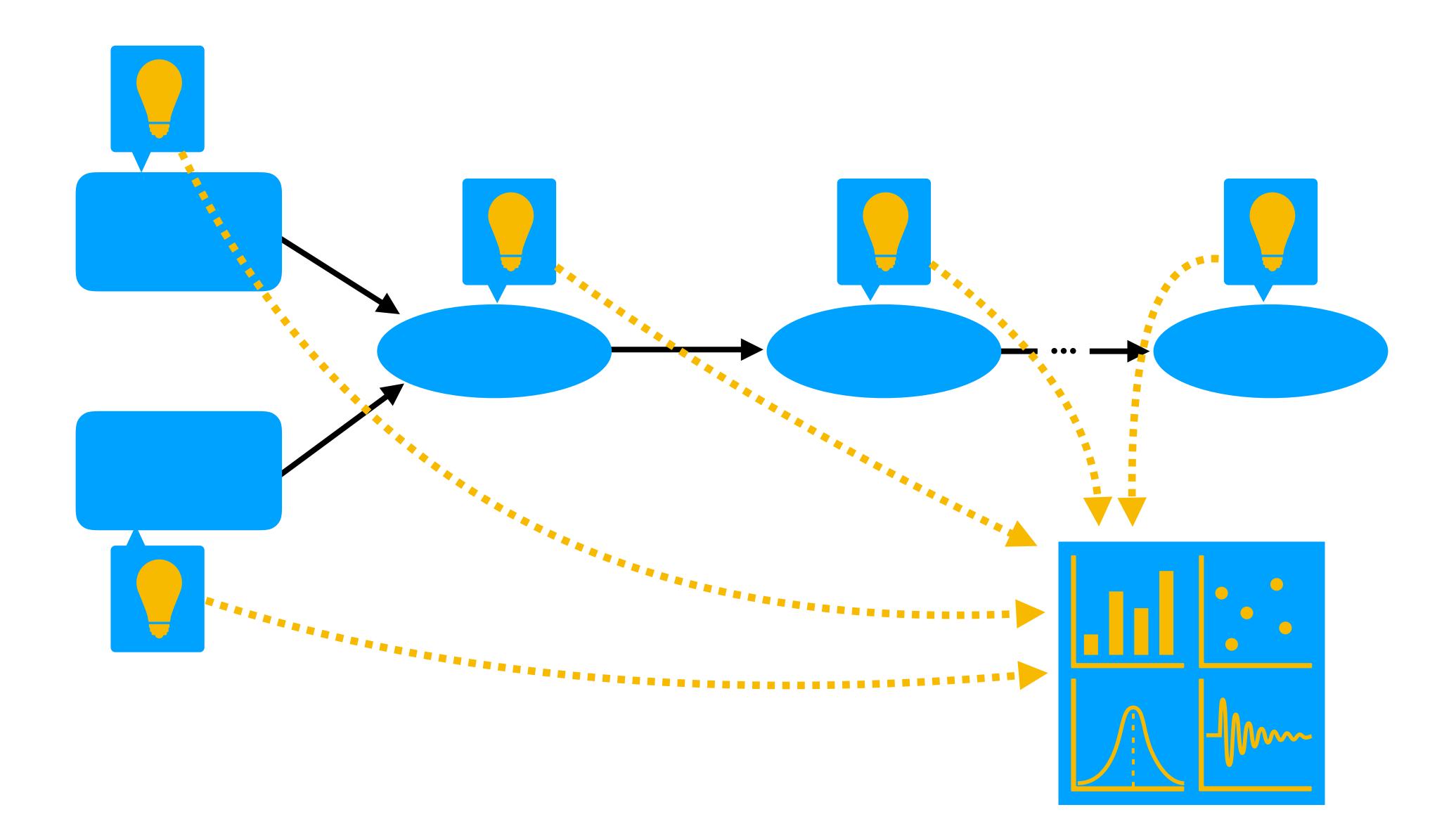












```
1 class MacCounter : private ExprVisitor {
    public:
 2
    MacCounter() {
 3
       count_ = 0;
 4
 5
 6
     static int64_t GetTotalMacNumber(const Expr& expr) {
 7
       LOG(INFO) << "This pass only counts MACs in direct CONV 2D, "
                 << "CONV 2D Transpose and Dense ops";
 8
 9
       MacCounter counter;
       counter(expr);
10
11
       return counter.count_;
12
13
    private:
14
15
     void VisitExpr_(const CallNode* call_node) final {
       static const auto& fprep =
16
           Op::GetAttr<FMacCount>("FMacCount");
17
       auto f = fprep.get(call_node->op, nullptr);
18
       if (f != nullptr) count_ += f(GetRef<Call>(call_node));
19
       ExprVisitor::VisitExpr_(call_node);
20
21
22
23
     int64_t count_;
24 };
```

```
1 class MacCounter : private ExprVisitor {
    public:
 2
    MacCounter() {
 3
       count_ = 0;
 4
 5
     static int64_t
 6
                        private:
       LOG(INFO) <<
 7
                    2
 8
                    3
       MacCounter c
 9
                     4
10
       counter(expr
       return count
11
                     5
12
                     6
13
14
    private:
     void VisitExpr
15
                   8
       static const
16
                    9
17
           Op::GetA
       auto f = fpr 10
18
       if (f != nul
19
20
       ExprVisitor:....
21
22
23
     int64_t count_;
24 };
```

1 class FindDef : private ExprVisitor {

VarMap<Expr> expr_map_;

void VisitExpr_(const LetNode* l) final { CHECK_EQ(expr_map_.count(l->var), 0); expr_map_[l->var] = l->value; VisitExpr(l->value); VisitExpr(l->body);



There's no Relay-sanctioned way to build program analyses!

• Duplication of effort

- Duplication of effort
- High barrier to entry for new developers

- Duplication of effort
- High barrier to entry for new developers
- Less readability and maintainability

[Relay][RFC] Analysis Infrastructure #3895



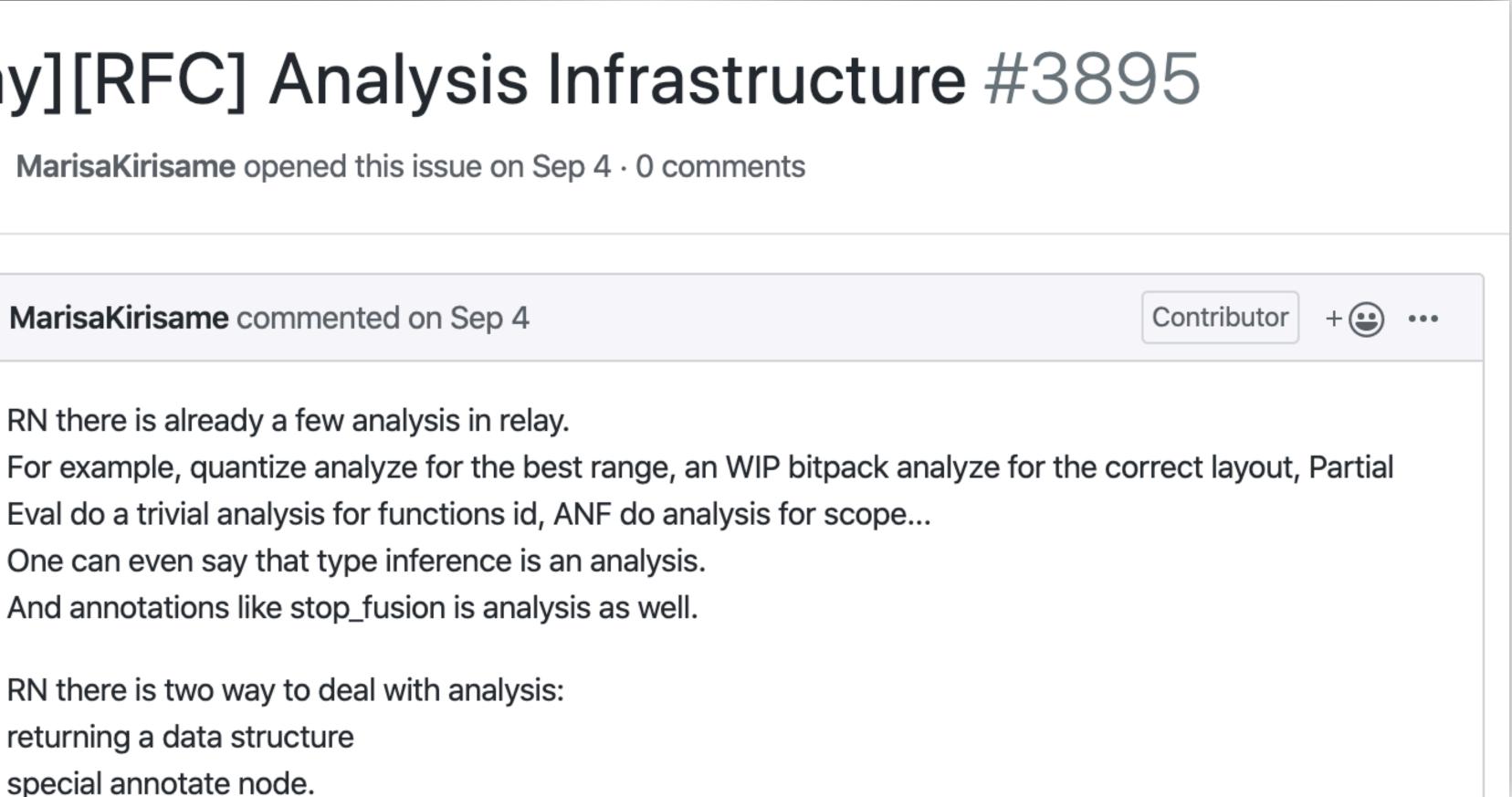
MarisaKirisame opened this issue on Sep 4 · 0 comments



MarisaKirisame commented on Sep 4

RN there is already a few analysis in relay. Eval do a trivial analysis for functions id, ANF do analysis for scope... One can even say that type inference is an analysis. And annotations like stop_fusion is analysis as well.

RN there is two way to deal with analysis: returning a data structure special annotate node.



[Relay][RFC] Analysis Infrastructure #3895

() Open

MarisaKirisame opened this issue on Sep 4 · 0 comments

[RFC] Data-flow Analysis Functionality on TVM IR #4468

() Open

DKXXXL opened this issue 4 hours ago · 4 comments

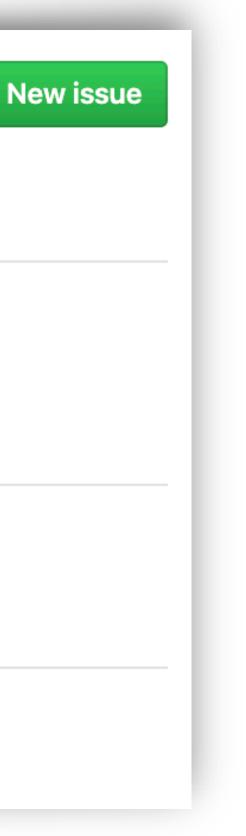


DKXXXL commented 4 hours ago • edited -

Problem

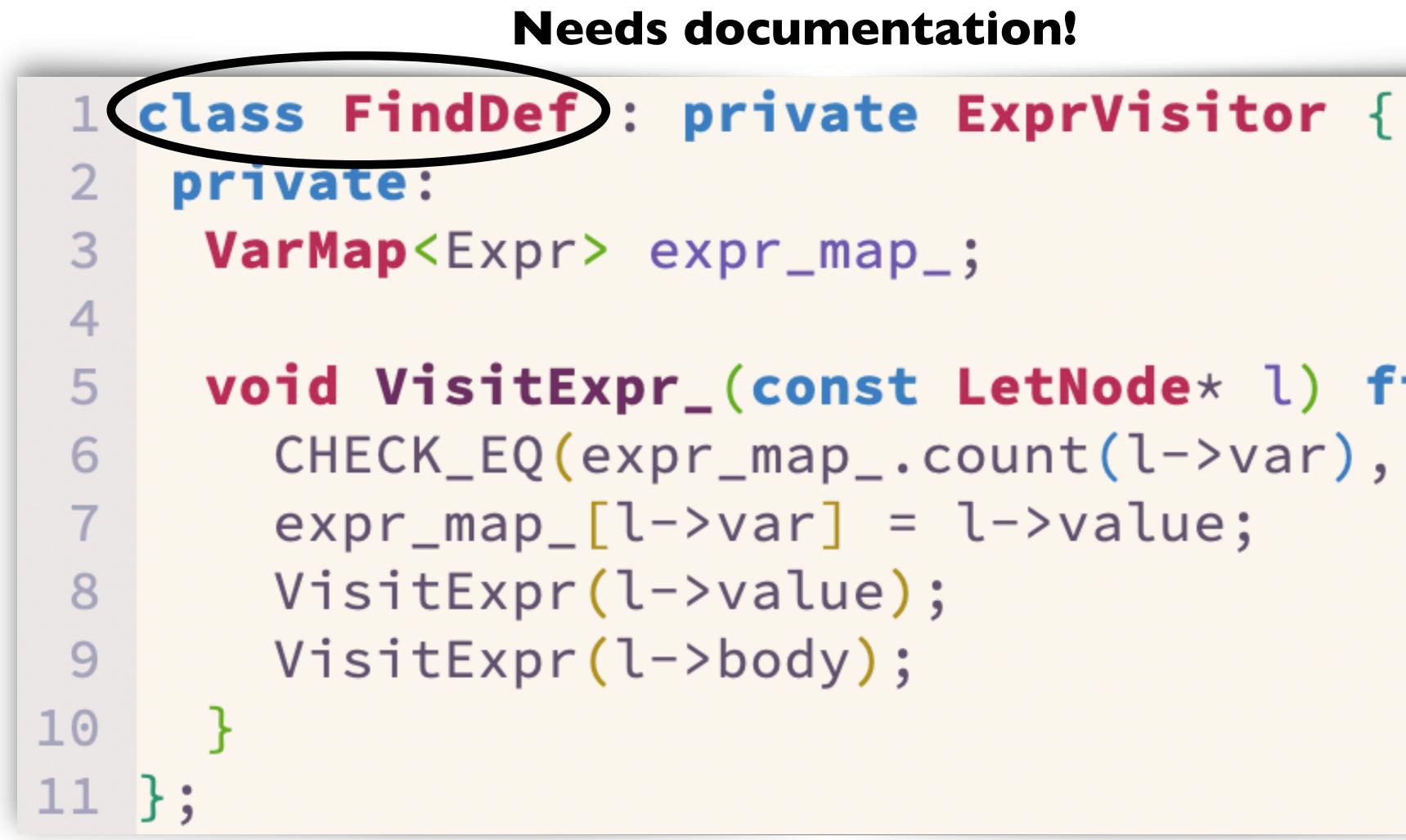
When developing program passes on TVM IR (the one once was of information requiring program analysis, for example, live varial This requirement becomes urgent when TVM has to directly issu processing stage (for example LLVM) **cannot analyze the progr**am

+ 😐 🚥	Assignees
	No one assigned
	Labels
s Halide IR), it is normal to ask for all sorts ble analysis for dead code elimination.	None yet
ue intrinsic and the subsequent r am because of these intrinsic .	Projects
	None yet



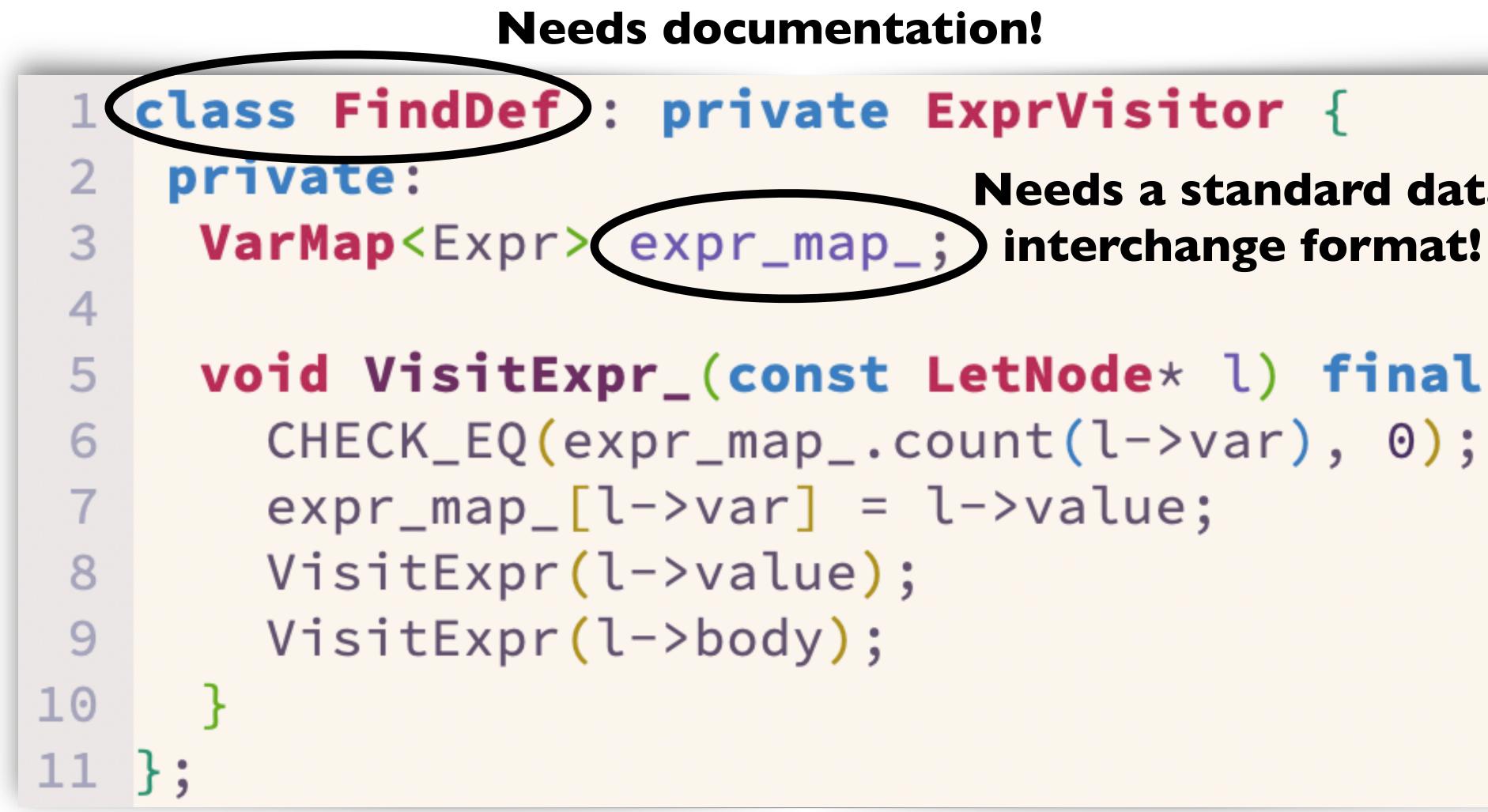
1 class FindDef : private ExprVisitor { private: 2 3 VarMap<Expr> expr_map_; 4 5 6 expr_map_[l->var] = l->value; 7 VisitExpr(l->value); 8 9 VisitExpr(l->body); 10 $11 \};$

```
void VisitExpr_(const LetNode* l) final {
  CHECK_EQ(expr_map_.count(l->var), 0);
```



Needs documentation!

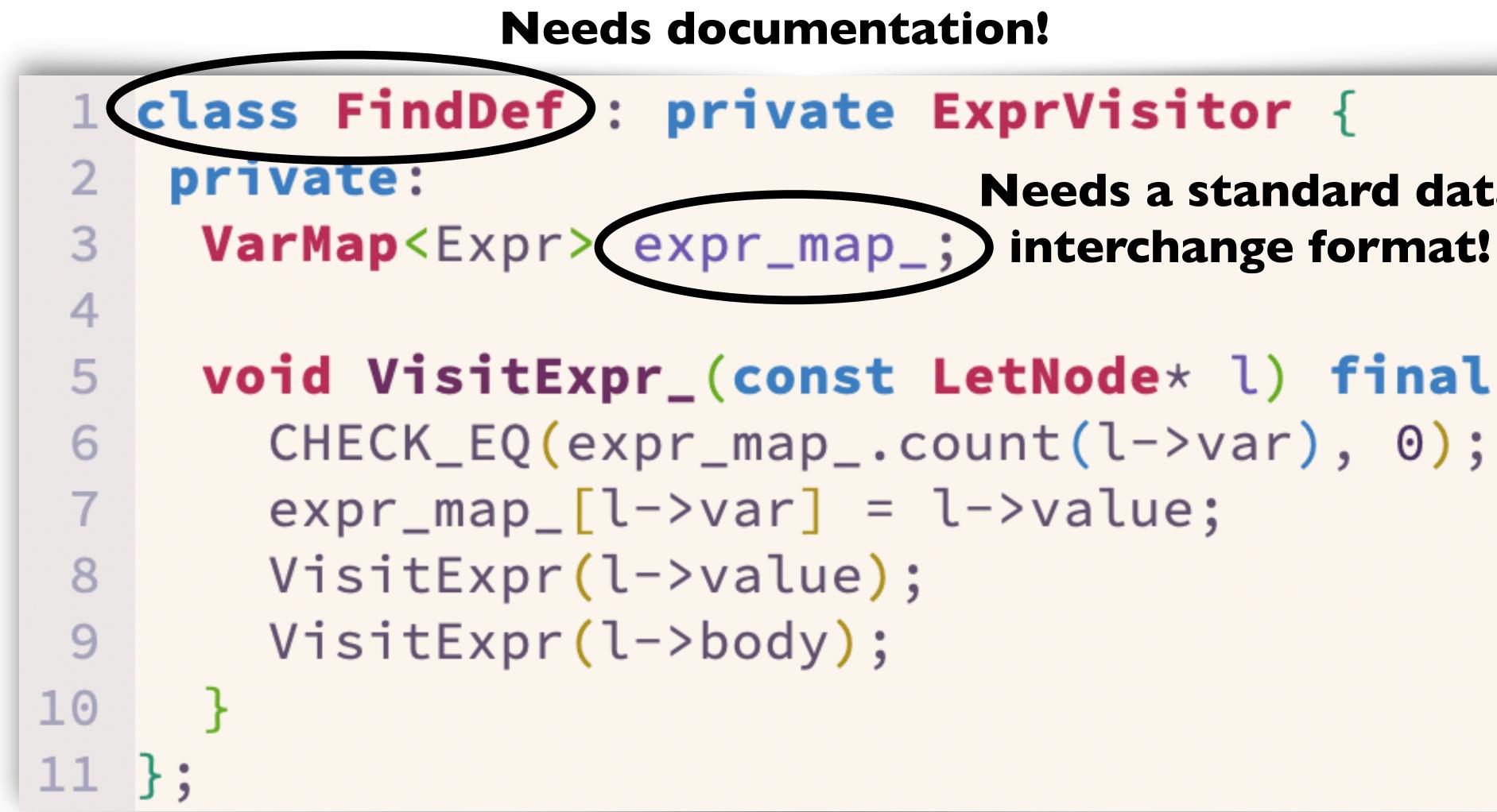
```
void VisitExpr_(const LetNode* l) final {
  CHECK_EQ(expr_map_.count(l->var), 0);
```



Needs documentation!

Needs a standard data

void VisitExpr_(const LetNode* l) final { CHECK_EQ(expr_map_.count(l->var), 0);



...and needs to be discoverable/accessible!

Needs documentation!

Needs a standard data

void VisitExpr_(const LetNode* l) final { CHECK_EQ(expr_map_.count(l->var), 0);

What do we want in an analysis framework?

What do we want in an analysis framework?

• Supports many types of program analyses

- analyses
- Quick to write new analyses

What do we want in an analysis framework?

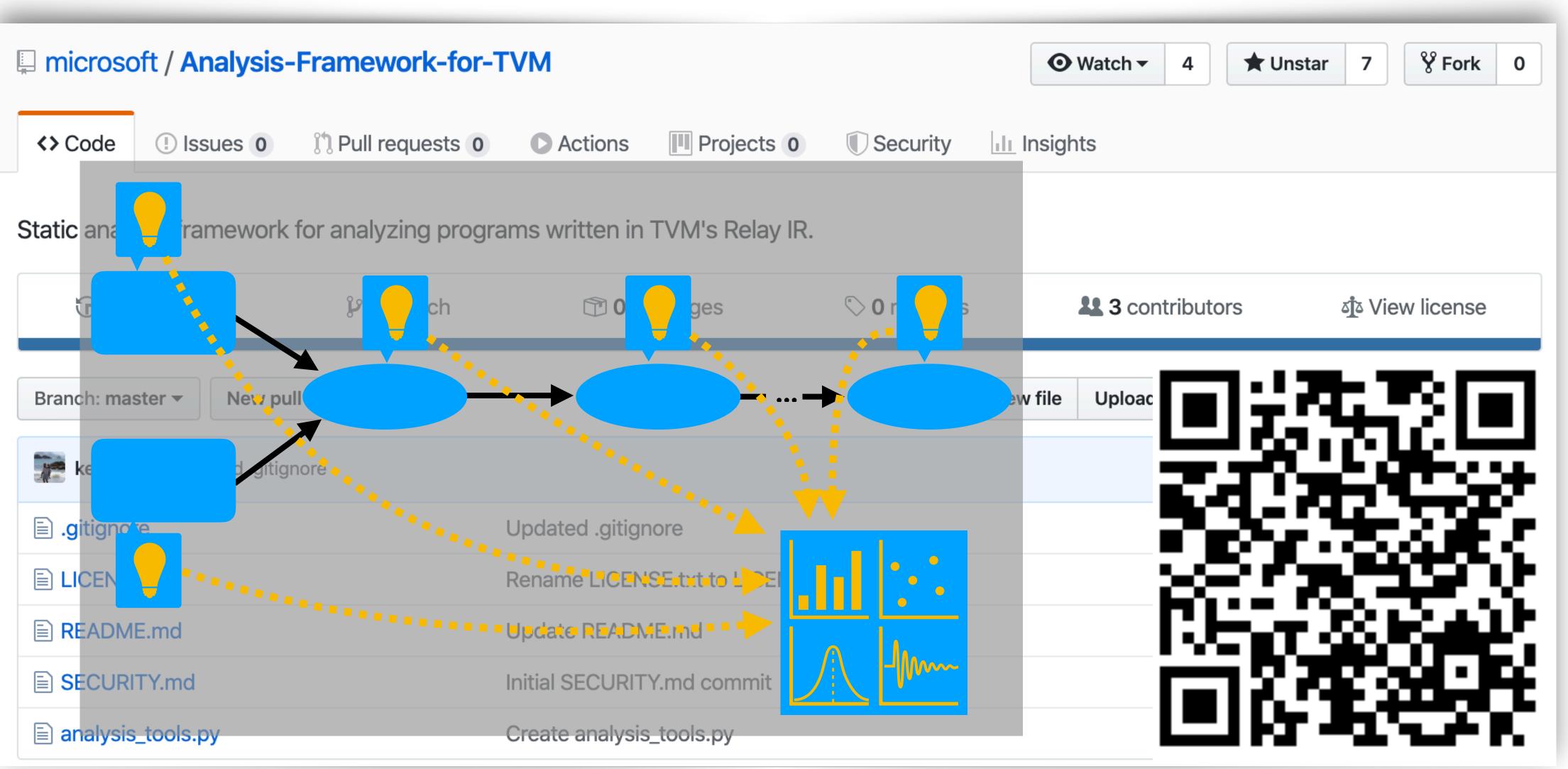
• Supports many types of program

- analyses
- Quick to write new analyses
- together

What do we want in an analysis framework?

• Supports many types of program

• Promotes composing analyses



https://github.com/microsoft/Analysis-Framework-for-TVM



```
508 lines (508 sloc) 16.4 KB
In [ ]: import tvm
         from tvm import relay
         import tvm.relay.analysis_tools
         We'll start by examining a simple Relay program:
In [ ]: program = relay.const(1) - (relay.var('x') * relay.var('y'))
         This simple analysis pass visits all Calls. It uses the AnalysisPass helper method _add_detail to attach analysis results to an
         expression. In this case, it attaches an analysis result named 'readable_name' to the Call being visited. _add_detail is one of
         the main conveniences added by this simple analysis framework.
In [ ]: class GetReadableName(relay.analysis_tools.AnalysisPass):
              def visit_call(self, call):
                  super().visit_call(call)
                  self._add_detail(call, readable_name=call.op.name)
```



https://github.com/gussmith23/tvm/blob/analysis-framework-demo/demo.ipynb



[RFC] Program Analysis Framework in Relay #4449

() Open

gussmith23 opened this issue 4 days ago · 0 comments



gussmith23 commented 4 days ago

Please also see #3895, which is @MarisaKirisame's RFC around a specific change to support analyses in Relay.

This RFC pertains to building a centralized, comprehensive program ana primary uses of program analyses in Relay: for generating analysis data things such as quantization, and for generating human-readable data us exploring Relay programs. Whereas Marisa's request pertains more to the inspired by the second use-case, and motivated by a desire to build a fracases. Such frameworks exist -- see LLVM's framework, which is design both the compiler and the developer.

I built a small analysis framework for Relay this past summer at Microsof readable analyses of Relay programs. A demo of this framework can be

<u>https://github.com/apache/incubator-tvm/issues/4449</u>





Contributor

+ 😐 🚥