Optimizing Sparse/Graph Kernels with TVM

Yuwei Hu
Advisor: Zhiru Zhang
Internship Mentor: Yida Wang
Graph Neural Networks (GNNs) are getting popular

Diverse Applications
1. AliGraph: A Comprehensive Graph Neural Network Platform
2. Interpolate between two molecules with pre-trained JTNN

Emerging Frameworks
3. https://www.dgl.ai
4. https://pytorch-geometric.readthedocs.io
5. https://github.com/PaddlePaddle/PGL
Two Key Kernels in GNNs

\[ h_1^{\text{new}} = f(\text{Aggregate}(h_2, h_3, h_4, h_5)) \]

Message-passing is doing SpMM (sparse-dense matrix multiply)

More precisely, it is SpMM-like if we use a customized aggregation(reduce) function other than sum

Dot-product attention is doing SDDMM (sampled dense-dense matrix multiply)
Challenges

Existing deep learning frameworks have very limited and inflexible support for sparse computation

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Exploring the Feature Dimension

CPU: feature dimension tiling to improve cache utilization

GPU: parallelization strategies specialized for computation patterns
Preliminary Results

Tested on c5.18xlarge instance with 36 cores and 140GB DRAM
Dataset is reddit with 233K vertices and 115M edges
Plan

Integration into DGL

Frontend: message passing programming interface in DGL

Backend: optimized sparse kernels written in TVM
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Native Sparse Support in TVM

Current implementation is using IR builder
Optimization techniques can be abstracted into schedule primitives. For example, we can introduce sparse split.
Ongoing efforts from UW to build an infrastructure for sparse representation and computation: [RFC]
Credits and Thanks

Jiali Yu @ SJTU, for helping with benchmarking

Andrew Tulloch @ Facebook, for contributing the blocked sparse kernel in TVM, which greatly inspired this work

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